Translation

PATENT COOPERATION TREATY



PCT Rec'd PCT/PTO 10 NOV 2005

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY (Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference					
JSONY-451PCT F	OR FURTHER ACTION	See Form PCT/IPEA/416			
PC1/JP2003/013832	ernational filing date (day/month/year) 29 October 2003 (29.10.2003)	Priority date (day/month/year) 20 November 2002 (20.11.2002)			
International Patent Classification (IPC) or nation G11C 11/407	al classification and IPC	20 November 2002 (20.11.2002)			
Applicant					
	SONY CORPORATION				
 This report is the international preliminar Authority under Article 35 and transmitte 	y examination report, established by this d to the applicant according to Article 36	International Preliminary Examining 5.			
2. This REPORT consists of a total of	5 sheets, including this cover s	heet.			
5. Inis report is also accompanied by ANNE	EXES, comprising:				
a (sent to the applicant and to the	International Bureau) a total of	sheets, as follows:			
sheets of the description and/or sheets containing Administrative Instruction	n, claims and/or drawings which have be g rectifications authorized by this Author ons).	en amended and are the basis of this report rity (see Rule 70.16 and Section 607 of the			
Supplemental Box.	apphoanon as med, a	considers contain an amendment that goes s indicated in item 4 of Box No. I and the			
b (sent to the International Barreadable form only, as indicate Administrative Instructions).	ureau only) a total of (indicate typ , containing a sequence listing d in the Supplemental Box Relating to	e and number of electronic carrier(s)) and/or tables related thereto, in computer Sequence Listing (see Section 802 of the			
4. This report contains indications relating to					
Box No. I Basis of the report					
Box No. II Priority					
Box No. III Non-establishment of opinion with regard to novelty, inventive step and industrial applicability					
Box No. IV Lack of unity of invention					
Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability;					
Box No. VI Certain documents cit	ed				
Box No. VII Certain defects in the	international application				
	on the international application				
Date of submission of the demand	Date of completion of the	nis report			
24 March 2004 (24.03.2004)		rch 2005 (20.03.2005)			
lame and mailing address of the IPEA/JP	Authorized officer				
acsimile No.	Telephone No.				
orm PCT/IPEA/409 (cover sheet) (January 2004)					

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

PCT/JP2003/013832

Box No	o. I	Basis of the report
1. With	h regare rwise i	i to the language, this report is based on the international application in the language in which it was filed, unless
	This which	report is based on translations from the original language into the following language, this language of a translation furnished for the purpose of:
<u> </u>		international search (under Rules 12.3 and 23.1(b))
}		publication of the international application (under Rule 12.4)
Ì		international preliminary examination (under Rules 55.2 and/or 55.3)
1		(wites states on a diamon so.s)
	are not	to the elements of the international application, this report is based on (replacement sheets which have been the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed"
		nternational application as originally filed/furnished
		escription:
1	pages	, as originally filed/furnished
1	pages pages	received by this Authority on
ļ		received by this Authority on
	the cl	aims:
	pages	, as originally filed/furnished
	pages	, as amended (together with any statement) under Article 19
	pages	received by this Authority on
	pages	received by this Authority on
	the dr	awings:
	pages	no originally. File d/F
	pages'	received by this Authority on as originally filed/furnished
'	pages*	
	a sequ	
		ence listing and/or any related table(s) - see Supplemental Box Relating to Sequence Listing.
. [_	
3	The an	nendments have resulted in the cancellation of:
		the description, pages
		he claims, Nos.
		he drawings, sheets/figs
	П.	he sequence listing (specify):
	Ħ.	any table(s) related to sequence listing (specify):
	· ب	system of the solution is still graph in the solution is such as the solution
	(Rule 7	port has been established as if (some of) the amendments annexed to this report and listed below had not been since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (0.2(c)). The description, pages
	^	ny table(s) related to sequence listing (specify):
* If item	4 appli	es, some or all of those sheets may be marked "superseded."
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International application No.

PCT/JP03/13832

Box No. V Reasoned statement	under Article 35(2) with		03/13832	
citations and explan	oned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability;			
1. Statement				
Novelty (N)	Claims	3-10, 12-14, 16-17		
•	Claims		YES	
Inventive step (IS)	C1 :	1, 2, 11, 15	NO NO	
	Claims	4-10	VEC	
•	Claims	1-3, 11-17	YES NO	
Industrial applicability (IA)	Claims	1.10	NO	
	Claims	1-17	YES	
			NO	

2. Citations and explanations (Rule 70.7)

Document 1: JP, 2001-135080, A (Matsushita Electric Industrial Co., Ltd.), 18 May, 2001 (18.05.01), paragraphs [0008], [0009] and [0013], Fig. 8, & US, 6349072, B1

Document 2: JP, 8-255107, A (Toshiba Corp.), 1 October, 1996 (01.10.96), full text, Fig. 3

Claims 1 and 2

The subject matters of claims 1 and 2 do not appear to be novel or to involve an inventive step, since they are described in document 1 (paragraphs [0008], [0009] and [0013] and Fig. 8) newly cited in the IPER. Claim 3

The subject matter of claim 3 does not appear to involve an inventive step in view of document 1 and document 2 cited in the ISR.

It is well known that the address of the memory cell in a request signal and the address of data temporarily held in an amplifier are compared to judge whether or not a page is hit.

Claims 4-10

The subject matters of claims 4-10 appear to involve an inventive step in view of documents 1 and 2. Documents 1 and 2 do not describe that when the address of a requested memory cell and the address of data of an amplifier are compared and found not to agree with each other, an instruction is given to collectively process charge storing action, charge taking out action and input/output action at a time with a single clock used as synchronous timing. This constitution exhibits an advantageous effect that highly efficient data processing

Claims 11 and 15

The subject matters of claims 11 and 15 do not appear to be novel or to involve an inventive step, since they are described in document 2 (Fig. 3).

Claims 12-14, 16 and 17

The subject matters of claims 12-14, 16 and 17 do not appear to involve an inventive step in view of documents 1 and 2.

As described in document 1 (paragraphs [0008], [0009] and [0013] and Fig. 8), a person skilled in the art could have easily combined a read/write cycle and a pre-charge cycle into one cycle in the case where the

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

PCT/JP03/13832

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Applicati Patent	ion No. t No.	Publication date (day/month/year)	Filing date (day/month/year)	Priority date (valid clair (day/month/year)
	259318 A , X]	16.09.2004	24.02,2003	
		,		
	osures (Rule 70.9			Date of written disclosure
Kind of no	on-written disclo		on-written disclosure r y/month/year)	Date of written disclosure referring to non-written disclosure (day/month/year)

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

PCT/JP03/13832

Box No. VIII Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made:

- 1. Claim 4 describes "in the order of ... the input/output action of the said amplifier for the outside of the said information storage and the charge taking out action for taking out the charges existing in the range from the said memory cell to the said amplifier," but this order is considered to be reverse.

 This applies also to claim 8.
- 2. In claim 8, "when ... agrees with" is considered to be an error for "when ... does not agree with."
- 3. Claim 10 describes as if the order of the input/output action and the charge storing action is changed in response to the frequency of a synchronous clock, but "The Best Modes for Carrying Out the Invention" of the present application describes to the effect that whether the input/output action and the charge storing action are carried out in different cycles or in one cycle is selected in response to the frequency of a synchronous clock. The descriptions do not match.

Form PCT/IPEA/409 (Box No. VIII) (January 2004)